

STC20DE90HV

Preliminary Data

General features

Table 1.General features

V _{CS(ON)}	I _C	R _{CS(ON)}
1.2 V	20 A	0.06 Ω

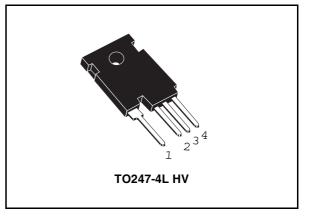
- Low equivalent on resistance
- Very fast-switch, up to 150 kHz
- Squared RBSOA, up to 900 V
- Very low C_{ISS} driven by $R_G = 47 \Omega$
- In compliance with the 2002/93/EC European Directive

Description

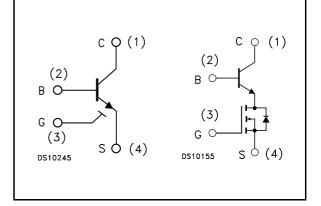
The STC20DE90HV is manufactured in a hybrid structure, using dedicated high voltage Bipolar and low voltage MOSFET technologies, aimed to providing the best performance in ESBT topology. The STC20DE90HV is designed for use in power supply forward converter and three-phase power factor corrector applications.

Applications

- SMPS forward converter
- Three-phase power factor corrector



Internal schematic diagrams



Order codes

Part Number	Number Marking Package		Packing	
STC20DE90HV	C20DE90HV	TO247-4L HV	Tube	

October 2006

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This is preliminary information on a new product now in development or undergoing evaluation. Details are subject to change without notice.

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Electrical ratings

Table 2.	Absolute maximum ratings		
Symbol	Parameter	Value	Unit
V _{CS(SS)}	Collector-source voltage (V _{BS} =V _{GS} =0V)	900	V
V _{BS(OS)}	Base-source voltage (I _C =0, V_{GS} =0V)	30	V
V _{SB(OS)}	Source-base voltage ($I_C = 0$, $V_{GS} = 0V$)	9	V
V _{GS}	Gate-source voltage	±20	V
Ι _C	Collector current	20	А
I _{CM}	Collector peak current (t _P < 5ms)	60	А
۱ _B	Base current	5	А
I _{BM}	Base peak current (t _P < 1ms)	20	А
P _{tot}	Total dissipation at $T_c \le 25^{\circ}C$	139	W
T _{stg}	Storage temperature	-40 to 150	°C
TJ	Max. operating junction temperature	150	°C

Table 3. Thermal data

Symbol	Parameter	Value	Unit
R _{thj-case}	Thermal resistance junction-case max	0.9	°C/W

Electrical characteristics

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Electrical characteristics

 $(T_{case} = 25^{\circ}C \text{ unless otherwise specified})$

Table 4.	Electrical	characteristics
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Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
I _{CS(SS)}	Collector-source current $(V_{BS} = V_{GS} = 0V)$	V _{CS(SS)} =900V			100	μA
I _{BS(OS)}	Base-source current (I _C =0, V _{GS} =0V)	V _{BS(OS)} =30V			10	μA
I _{SB(OS)}	Source-base current (I _C =0, V _{GS} =0V)	V _{SB(OS)} =9V			100	μA
I _{GS(OS)}	Gate-source leakage (V _{BS} =0V)	$V_{GS} = \pm 20V$			500	nA
V _{CS(ON)}	Collector-source ON voltage	$V_{GS} = 10V$ $I_{C} = 20A$ $I_{B} = 4A$ $V_{GS} = 10V$ $I_{C} = 10A$ $I_{B} = 1A$		1.2 0.65		V V
h _{FE}	DC current gain	$V_{CS} = 1V$ $V_{GS} = 10V$ $I_{C} = 20A$ $V_{CS} = 1V$ $V_{GS} = 10V$ $I_{C} = 10A$		4 12		
V _{BS(ON)}	Base-source ON voltage	$V_{GS} = 10V I_C = 20A I_B = 4A$ $V_{GS} = 10V I_C = 10A I_B = 1A$		1.8 1.2		V V
V _{GS(th)}	Gate threshold voltage	$V_{BS} = V_{GS}$ $I_B = 250 \mu A$	1.5	2.2	3	V
C _{iss}	Input capacitance	$V_{CS} = 25V$ f = 1MHz $V_{GS} = V_{CB} = 0V$		750		pF
Q _{GS(tot)}	Gate-source Charge	$V_{CS}=25V$ $V_{GS}=10V$ $V_{CB}=0V$ $I_{C}=20A$		12.5		nC
t _s t _f	INDUCTIVE LOAD Storage time Fall time	$V_{GS} = 10V$ $R_G = 47\Omega$ $V_{Clamp} = 720V$ $t_p = 4\mu s$ $I_C = 10A$ $I_B = 2A$		775 7		ns ns
t _s t _f	INDUCTIVE LOAD Storage time Fall time	$\label{eq:GS} \begin{array}{ll} V_{GS} = 10V & R_{G} = 47\Omega \\ V_{Clamp} = 720V & t_{p} = 4\mu s \\ I_{C} = 10A & I_{B} = 1A \end{array}$		510 5		ns ns
V _{CS(dyn)}	Collector-source dynamic voltage (500ns)	$\label{eq:V_CC} \begin{split} & V_{CC} = V_{Clamp} = 400V \\ & V_{GS} = 10V & I_{C} = 10A \\ & I_{B} = 2A & R_{G} = 47\Omega \\ & t_{peak} = 500ns & I_{Bpeak} = 10A \end{split}$		2.3		V



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		•				
Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
V _{CS(dyn)}	Collector-source dynamic voltage (1µs)	$V_{CC} = V_{Clamp} = 400V$ $V_{GS} = 10V \qquad I_C = 10A$ $I_B = 2A \qquad R_G = 47\Omega$ $t_{peak} = 500ns \qquad I_{Bpeak} = 10A$		1		V
V _{CSW}	Maximum collector- source voltage switched without snubber	$R_{G} = 47\Omega$ $h_{FE} = 5$ $I_{C} = 20A$	900			V

Figure 2.

V_{CE(sat)dyr}

(V)

5

4

3

2

 $I_{c} = 15A$

2

 Table 4.
 Electrical characteristics

Note (1) Pulsed duration = 300 μ s, duty cycle \leq 1.5%

2.1 Electrical characteristics (curves)

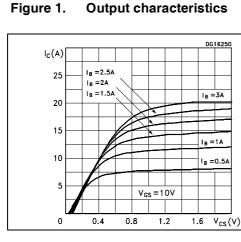


Figure 3. DC current gain



0

Gate threshold voltage vs temperature

6

8 t(μs)

Dynamic collector-source

 $h_{FE} = 5$

 $V_{GS} = 10V$

 $R_G\!=\!47\,\Omega$

 $|_{Bpeack} = 2|_{C}$

 $V_{CC} = V_{Clamp} = 400V$

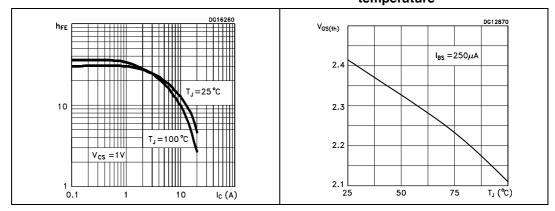
DG16310

saturation voltage

 $I_c = 10A$

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I_c =5A



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Electrical characteristics

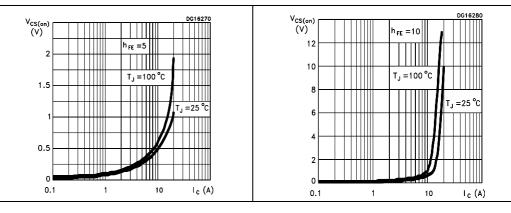


Figure 5. Collector-source On voltage Figure 6. **Collector-source On voltage**

Figure 7. **Base-source On voltage**





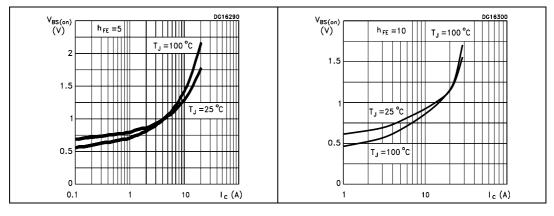
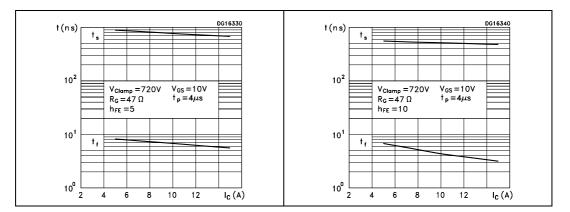


Figure 9. Inductive load switching time Figure 10. Inductive load switching time



Electrical characteristics

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Figure 11. Reverse biased safe operating area				
			DG16320	
l _c (A)				
20				
15	V _{GS} =	=10V		
10	R _g =4	47 Ω		
5				
0	200 4	00 600	800 V _{cs} (/)



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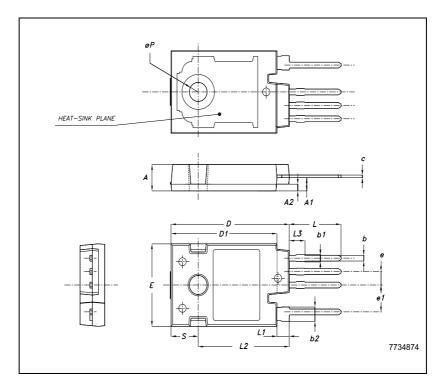
3 Package mechanical data

In order to meet environmental requirements, ST offers these devices in ECOPACK® packages. These packages have a Lead-free second level interconnect. The category of second level interconnect is marked on the package and on the inner box label, in compliance with JEDEC Standard JESD97. The maximum ratings related to soldering conditions are also marked on the inner box label. ECOPACK is an ST trademark. ECOPACK specifications are available at: www.st.com



DIM.		mm.	
лм. <u> </u>	MIN.	ТҮР	MAX.
A	4.85		5.15
A1	2.20	2.50	2.60
A2		1.27	
b	0.95	1.10	1.30
b2	2.50		2.90
С	0.40		0.80
D	23.85	24	24.15
D1		21.50	
E	15.45	15.60	15.75
е	2.54		
e1	5.08		
L	10.20		10.80
L1	2.20	2.50	2.80
L2		18.50	
L3		3	
øP	3.55		3.65
S		5.50	

TO247-4L HV MECHANICAL DATA





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4 Revision history

Table 5.Revision history

Date	Revision	Changes
10-Oct-2006	1	First release.



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